



## JANSONS INSTITUTE OF TECHNOLOGY

Karumathampatti, Coimbatore-641659

### Workshop Schedule

Program/Branch(Specialization)/Semester BE / Electronics And Communication Engineering / IV Sem  
 Batch / Section 2016 - 2020  
 Academic Year 2017 – 2018  
 Name of the Program Workshop on “Digital Circuit Design using EDA tools”  
 Name of the Faculty/ Organization providing the Training Mr.S.Karthikeyan, Mr.G.Myilsamy AP/ ECE JIT

Sl. No.	Date	Time Duration	Venue	Topics/Major contents to be Covered	Activity planned (Lecture/ Practical/ Evaluation)
1	07.03.18	9:00 AM to 10:30 AM	VLSI Lab	Introduction to Digital Logic circuits	THEORY
2	07.03.18	10:45 AM to 12:10 PM	VLSI Lab	Introduction to Verilog HDL(Operators and Modeling Styles)	THEORY
3	07.03.18	1:00 PM to 03:30 PM	VLSI Lab	Design of Digital Logic Circuits using Verilog HDL	PRACTICAL
4	08.03.18	9:00 AM to 10:30 AM	VLSI Lab	VLSI Design Flow	THEORY
5	08.03.18	10:45 AM to 12:10 PM	VLSI Lab	Introduction to CMOS Circuits	PRACTICAL
6	08.03.18	1:00 PM to 03:30 PM	VLSI Lab	Design of CMOS Circuits using Microwind	PRACTICAL