



# JANSONS INSTITUTE OF TECHNOLOGY

AC-32

Karnmathampatti, Coimbatore-641659

## Workshop Schedule

Program/Branch

BE / ELECTRONICS AND COMMUNICATION ENGINEERING/ VI 2016 - 2020

(Specialization)/Semester Batch / Section

2018 – 2019

Academic Year

EMBEDDED SYSTEM DESIGN –ARM (LPC2148-32bit Processor)

Name of the Program

Name of the Faculty/ Organization providing the Training

Mr.G.Myilsamy, Mr.P.Gowtham, Mrs.P.Eswari, & Ms.M.Surya. AP/ECE- JIT.

Date: 26.12.18

Sl. No.	Date	Time Duration	Venue	Topics/Major contents to be Covered	Activity planned (Lecture/ Practical/ Evaluation)
1	27.12.2018	9.00 AM to 10.30 AM	Networks Lab	Introduction to ARM processor	LECTURE
2	27.12.2018	10.45 AM to 12.15 PM	Networks Lab	LPC2148 Architecture and its applications	LECTURE
3	27.12.2018	1.00 PM to 3.30 PM	Networks Lab	Keil.C Integrated development environment (IDE) Tool.	PRACTICAL
4	28.12.2018	1.00 PM to 2.30 PM	Networks Lab	Blinking led inter facing with LPC 2148 Microcontroller	PRACTICAL
5	28.12.2018	02.40 PM to 3.30 PM	Networks Lab	Stepper Motor Interfacing and Buzzer interfacing with LPC 2148 Microcontroller	PRACTICAL

HOD/ECE