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Question Paper Code : 42384

B.E./B.Tech. DEGREE EXAMINATION, APRIL/MAY 2018

Fifth Semester

Computer Science and Engineering

CS 2304 – SYSTEM SOFTWARE

(Common to Information Technology)

(Regulations 2008)

(Also common to PTCS 2304 – System Software for B.E. (Part Time) Fourth Semester – CSE – Regulations 2009)

Time : Three Hours

Maximum : 100 Marks

Answer ALL questions

PART – A

(10×2=20 Marks)

1. Give examples of system software.
2. List some of the addressing modes supported by SIC/XE.
3. Name some of the data structures used in an assembler.
4. Define load and go assembler.
5. What is a bootstrap loader ?
6. What is dynamic linking ?
7. Differentiate between a macro and a subroutine.
8. What is the use of macro time variable ?
9. What are the common editing features associated with text editors ?
10. Name some of the output devices.



PART - B

(5×16=80 Marks)

11. a) Explain the SIC machine architecture with suitable diagram. (16)
(OR)
b) Describe the SIC/XE machine architecture, with suitable diagram. (16)
12. a) i) Explain the functions of two pass assembler. (8)
ii) Write a note on symbol table. (8)
(OR)
b) i) Explain briefly the principles of program relocation. (8)
ii) Describe about MASM assembler. (8)
13. a) i) Explain the characteristics of an absolute loader. (8)
ii) Write an algorithm for an absolute loader. (8)
(OR)
b) i) Write a note on linking editors. (8)
ii) Discuss briefly about the MS-DOS Linker. (8)
14. a) Explain the data structures used by the macro processor. (16)
(OR)
b) i) Discuss about the keyword macro parameters. (8)
ii) Discuss about the MASM Macro processors. (8)
15. a) Explain the overview of an editing process. (16)
(OR)
b) Explain about the interactive debugging systems. (16)