# ANNA UNIVERSITY COIMBATORE <br> <br> B.E. / B.TECH. DEGREE EXAMINATIONS : JAN - FEB 2009 <br> <br> B.E. / B.TECH. DEGREE EXAMINATIONS : JAN - FEB 2009 <br> <br> REGULATIONS : 2007 <br> <br> REGULATIONS : 2007 <br> <br> SECOND SEMESTER - CSE / IT <br> <br> SECOND SEMESTER - CSE / IT <br> <br> 070290003 /4EC1202 - BASICS OF ELECTRONICS ENGINEERING 

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## PART - A

## ANSWER ALL QUESTIONS

21. Define the terms conductivity and mobility in a semiconductor.
22. Write the volt-ampere equation for a PN junction diode. .
23. Distinguish between avalanche and Zener diode mechanisms.
24. Define the terms i) peak inverse voltageii) ripple factor
25. Define the term stability factor.
26. Mention the advantages of collector to base bias.
27. What is early effect?
28. What is thermal"rinaway?
29. Draw an emitter follower circuit?
30. State Barkhausen criterion for oscillation
31. What makes the crystal oscillator produce stable oscillations?
32. Determine the frequency of an Hartley oscillator if $L 1=100 \mu \mathrm{H}, \mathrm{L} 2=1 \mathrm{mH}$, the
coefficient of mutual inductance between $L 1$ and $L 2$ is $20 \mu \mathrm{H}$ and $C=20 \mathrm{Pf}$.
33. State Demorgan's theorem.
34. Prove that $X . X=X$.
35. Write the reduced expressions for $A=X Z+X W+Y Z+Y W$.
36. Realize the expression using NAND gates $C=X Y+Y W$.
37. What is a latch?
38. What are pulse triggered devices?
39. State any two applications of shift registers
40. What is D flipflop?

PART - B

## ANSWER ANY FIVE QUESTIONS

21.i) Discuss the working of a PN junction diode under forward and reverse bias. (6)
ii) Show that a Zener diode can be used as a voltage regulator.
22.i) Draw the circuit diagram of a CE amplifier and explain its working?
ii) Explain how transistor can be operated as a switch.
23.i) What is thermal runaway? State how it can be avoided?
ii) Draw a comparison between CE, CB and CC transistor configurations?
24. Derive the equation for power output and conversion efficiency of a Class $A$ amplifier.
25. Derive the frequency of oscillation in an RC phase shift oscillator with a neat circuit.
26. Convert the binary number 110011001100 to
i) decimal
ii) octal
iii) hexadecimal
iv) Gray code
$(4 \times 3=12)$
27. Reduce the following equation to MSP form using Karnaugh map

$$
\begin{equation*}
Z=X[\overline{Y Q} \overline{+}+Y(W Q+\bar{W})]+X Y W Q \tag{12}
\end{equation*}
$$

28.i) Explain the working of a JK flipflop.
ii) Draw the diagram of a decade counter and explain its operation.

