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## Question Paper Code: 91444

## B.E./B.Tech. DEGREE EXAMINATIONS, NOVEMBER/DECEMBER 2019

Third Semester

Electronics and Communication Engineering EC 6304 – ELECTRONIC CIRCUITS – I (Regulations 2013)

Time: Three Hours

Maximum: 100 Marks

Answer ALL questions

PART - A

(10×2=20 Marks)

- 1. What is an operating point?
- 2. Define Thermal runaway.
- 3. What is a cascade amplifier?
- 4. What does bootstrapping mean?
- 5. Define BiMOS.
- 6. Draw the small signal model of JFET.
- 7. State Miller's Theorem.
- 8. What is meant by gain-bandwidth product?
- 9. Define CMRR.
- 10. What are the advantages of MOSFET amplifiers?

(15)

(1×15=15 Marks)

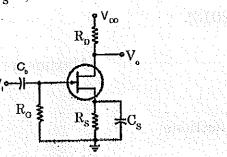
 $(5\times13=65 \text{ Marks})$ PART - B

11. a) With neat diagram explain the different types of biasing of JFET. (13)

(OR)

b) The amplifier shown in Fig., n-channel FET for which  $I_{\rm D}$  = 0.8 mA,  $V_{\rm p}$  = -20 V and  $I_{DSS} = 1.6$  mA. Assume that  $r_d > R_d$ . Find:

- **(5)** i) V<sub>GS</sub>. **(4)**
- · ii) g<sub>m</sub>.
- **(4)** iii) R<sub>s</sub>.



12. a) With neat diagram, explain BJT Differential Amplifier and its different modes of operation.

- (13)b) Explain Bootstrap Emitter Follower technique.
- 13. a) With neat diagram, explain Common source JFET amplifier with self-bias. (13)

(OR)

- b) Explain the small signal analysis of Common-Gate MOSFET amplifier. (13)
- (13)14. a) Explain the Low frequency analysis of BJT amplifier.

(OR)

- b) Draw a two stage RC coupled amplifier and derive the expression for upper (13)and lower cut-off frequencies.
- (13)15. a) Explain current steering circuit using MOSFET.

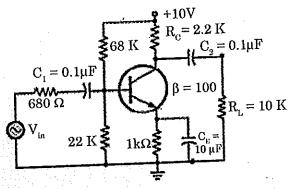
(OR)

b) Explain CMOS differential amplifier and also write the expression for (13)Differential mode gain and common mode gain.

16. a) Derive and determine the low frequency response parameters of the amplifier circuit shown in the figure.

Also draw the low frequency response of it.

(13)



(OR)

b) i) Derive expressions and analyze the IC MOSFET amplifier with active (8) load, enhancement load and depletion load.

ii) Give brief notes on BiMOS cascode amplifier.

(7)