#### ANNA UNIVERSITY OF TECHNOLOGY, COIMBATORE

#### B.E. / B.TECH. DEGREE EXAMINATIONS : NOV / DEC 2011

### **REGULATIONS: 2008**

### FOURTH SEMESTER

### 080250010 - MICROPROCESSORS AND MICROCONTROLLERS

### (COMMON TO CSE / IT)

Max.Marks: 100

12. (a)

Time : 3 Hours

### PART - A

 $(10 \times 2 = 20 \text{ Marks})$ 

## ANSWER ALL QUESTIONS

- What is the need for system clock and how it is generated in 8085? 1.
- Define opcode and operand. 2.
- 3 What are the modes in which 8086 can operate?
- 4 What is the data and address size in 8086?
- 5. What do mean by closely coupled configuration?
- 6 List the features of 8089
- What are the control words of 8251A and what are its functions? 7.
- How a keyboard matrix is formed in keyboard interface using 8279? 8
- What is mean by microcontroller? 9
- List out the interrupts of 8051 microcontroller? 10

## PART - B

### $(5 \times 16 = 80 \text{ MARKS})$

### ANSWER ALL QUESTIONS

Describe the functional pin diagram of 8085. 11. (a)

(OR)

Describe the functional block diagram of 8085. 11. (b)

# Explain the addressing modes and Macros of 8086? (OR) Describe the functional pin diagram of 8086. 12. (b) 13. (a) Explain in detail the coprocessor configuration (OR)

- Describe the functional block diagram of 8089. 13. (b)
- i) Explain the working of timer and write a program using it to generate a 14. (a) square waveform of period 3 msec. (10)
  - ii) Describe with any one of the mode configurations of timer in detail. (6) (OR)
- With necessary circuit diagram explain how a stepper motor is controlled 14. (b) using a microprocessor
- Write a program to subtract 2 8-bit numbers & exchange the digits using 15. (a) 8051?

## (OR)

Explain the architecture of 8051 microcontroller with necessary diagram 15. (b)

#### \*\*\*\*\*THE END\*\*\*\*\*

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