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## Question Paper Code : 97057

B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2014.

Third Semester
Electrical and Electronics Engineering EC 6202 - ELECTRONIC DEVICES AND CIRCUITS
(Common to Third Semester Electronics and Instrumentation Engineering, Instrumentation and Control Engineering, Robotics and Automation Engineering and also Common to Second Semester Biomedical Engineering and Medical Electronics Engineering)
(Regulation 2013)
Time : Three hours
Maximum : 100 marks
Answer ALL questions.

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\text { PART A }-(10 \times 2=20 \text { marks })
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1. What is transition capacitance and Diffusion capacitance?
2. What is a rectifier and list its types?
3. What is meant by biasing?
4. Compare BJT and FET.
5. Define the four h-parameters.
6. For an amplifier, midband gain $=100$ and lower cut-off frequency is 1 kHz . Find the gain of an amplifier at a frequency of 20 Hz .
7. Write down the need of cascading the amplifiers.
8. What is CMRR? List the various methods of improving CMRR.
9. Which is the most commonly used feedback arrangement in cascaded amplifier and why?
10. State the Barkhausen criterion for an oscillator.

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\text { PART B }-(5 \times 16=80 \text { marks })
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11. (a) With neat sketch explain the construction, operation and its characteristics of PN junction diode. Also list its advantages, disadvantages and its applications.

Or
(b) Explain the working of bridge rectifier. Give the expressions for RMS current, PIV, Ripple factor and efficiency.
12. (a) Explain the construction and operation of NPN transistor with neat sketch. Also comment on the characteristics of NPN transistor.

## Or

(b) With neat sketch, explain the construction, operation and characteristics of SCR.
13. (a) Draw the ' $h$ ' parameter equivalent circuit for a typical common emitter amplifier and derive expression for $\mathrm{Ai}, \mathrm{Ri}, \mathrm{Av}$ and Ro.

## Or

(b) (i) Derive the expression for the voltage gain of CS amplifier.
(ii) For CS amplifier, the operating point is defined by $V_{G S Q}=-2.5 \mathrm{~V}$, $V_{p}=-6 \mathrm{~V}$ and $I_{d Q}=2.5 \mathrm{~mA}$ with $I_{D S S}=8 \mathrm{~mA}$. Also $R_{G}=1 \mathrm{M} \Omega$, $R_{S}=1 \mathrm{~K} \Omega, \quad R_{D}=2.2 \mathrm{~K} \Omega$ and $V_{D D}=15 \mathrm{~V}:$ Calculate $g_{m}, r_{d}, Z_{i}, Z_{o}$ and $A_{\mathrm{v}}$.
14. (a) With neat sketch explain two stage cascaded amplifier and derive its overall Av, AI, RI and Ro.

## Or

(b) Draw a differential amplifier and its ac equivalent circuit. Derive for $A_{d}$ and $A_{C}$.
15. (a) Determine $R_{i f}, R_{o f}, A_{v}$ and $A_{v f}$ for the following:
(i) Voltage shunt feedback amplifier
(ii) Current series feedback amplifier.

## Or

(b) Explain the following with neat diagram.
(i) RC phase shift oscillator
(ii) Hartley oscillator.

