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Reg. No.:						,	1	

Question Paper Code: 50475

B.E./B.Tech. DEGREE EXAMINATION, NOVEMBER/DECEMBER 2017 Third Semester

Electrical and Electronics Engineering

EE 6303 - LINEAR INTEGRATED CIRCUITS AND APPLICATIONS

(Common to : Electronics and Instrumentation Engineering/Instrumentation and Control Engineering)

(Regulations 2013)

Time: Three Hours

Maximum: 100 Marks

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more and the figure of Answer ALL questions and present assume that

PART - A

(10×2=20 Marks)

- 1. State the limitations of IC technology. He says the say of the last actions a graded the for
- 2. Distinguish between dry etching and wet etching.
- 3. A 100 pF capacitor has maximum charging current of 100 microampere. Calculate its slew rate.
- 4. What is the drawback of IC 741?
- 5. What is the use of sample and hold circuit?
- 6. Write any two applications of clipper and clamper.
- 7. Define PULL time of PLL.
- 8. Where is the analog multiplier circuit used?
- 9. What is an isolation amplifier?
- 10. List the characteristics of opto coupler.

PART - B

 $(5\times13=65 \text{ Marks})$

- 11. a) Explain the basic process used in silicon planar technology with neat diagram. (13) (OR)
 - b) Write a note on classification of IC and IC packages.

(13)



12. a) Explain the following to	erms in an OP-AMP :		
i) Bias current		,	(3)
ii) Thermal drift	taja ngawat taga kan dalam dalam Banggaran dalam	for the American Comment of the Comm	(3)
iii) Input offset voltage	and current		(4)
iv) Virtual ground.	And the second		(3)
(OR)		range (1985) in the second of	• • •
b) Draw the circuit of a sy			
derive for CMRR.			(13)
13. a) With neat diagram, exp	lain the working prin	ciple of	
i) R-2R ladder type DA	AC		(7)
ii) Weighted resistor D	AC		(6)
(OR)			
b) Draw and explain the c derive its transfer func	•	er Butterworth low pas	ss filter and (13)
14. a) i) Briefly explain the dif	ference between the ty	vo operating modes of 5	55 Timer. (7)
ii) List the important fe	the second of th	+ * *	
(OR)			•
b) Write a note on:			· · · · · · · · · · · · · · · · · · ·
i) Analog multipliers	ii) VCO.	•	(8 + 5)
 a) Briefly explain the worki circuit diagrams and wa 		mode power supply wit	h necessary (13)
(OR)			
b) Write short notes on:			
i) LM 380 Power Audio	o Amplifier	•	(6)
ii) ICL 8038 Function g	generator IC.		(7)
	PART - C	(1×	15=15 Marks)
a) What are the new trend its scope for future gene		t technologies and exp	olain about
(OR)			
b) Explain in detail the re	cent fabrication meth	ods of diode and cana	citance for
industrial applications	OULD EURITOURIUM MICH	ious or arous and supe	OLUMINO 401